a gate line provided on a substrate;

a data line provided on said substrate;

a wiring provided on said substrate;

a pixel electrode provided on said substrate and superposed on said gate line with an insulator therebetween to form a first capacitance and superposed on said wiring with an insulator therebetween to form a second capacitance; and

at least one transistor provided on said substrate and connected with said gate line at a gate thereof and connected with said data line at one of source and drain thereof and connected with said pixel electrode at the other one of the source and drain wherein said wiring is supplied with signals having an opposite polarity to those applied to said gate wiring.

26. The device of claim 25 wherein the signals applied to said wiring have the same magnitude of voltage as the signals applied to said gate line.--

REMARKS

The Examiner's Official Action dated June 12, 1996 has been received and its contents carefully noted. Claim 23 has been canceled and new claims 25-26 have been added to more clearly define protection to which applicant is entitled. Claims 1, 5, 6 and 21-23 are independent. Accordingly, claims 1-3, 5-8, 21-22 and 24-26 are now pending in the present application and, for the reasons set forth in detail below, are believed to be in condition or allowance.

The present invention is characterized by forming a pixel electrode superposed on both (1) a gate line with an insulator therebetween and (2) a wiring with an insulator therebetween and two capacitances of the same magnitude are formed thereby. New claims 25-26 recite that the wiring is supplied with signals having an opposite polarity and the same magnitude as those applied to the gate wiring. This feature is discussed in detail